

## **Workshop - Content**

### **Wednesday, September 26<sup>th</sup>, 2012**

#### **Specification-driven Design with IC 6.1.5 ADE-XL**

**Presenter: B. Sc. A. H. Osman, Principal Services AE, Cadence Design Systems**

- Simulation tests authoring use-model
- Simulation management infrastructure
- Corners and sweeps analysis
- Device parameterization
- Simulation database post-processing
- Specification driven automatic design documentation creation

### **Thursday, September 27<sup>th</sup>, 2012**

#### **Variability Aware Design and Analysis with IC 6.1.5 ADE-GXL**

**Presenter: B. Sc. A. H. Osman, Principal Services AE, Cadence Design Systems**

- Design variability background (corners versus statistics)
- Corner analysis
- Statistical sensitivity analysis
- Parameter optimization
- Worst case distance corners extraction and yield optimization

#### **Constraint-driven Analog Rapid Prototyping with IC 6.1.5**

**Presenter: Dipl.-Ing., Marc Heise, Sales Technical Leader AE, Cadence Design Systems**

- Assisted physical constraint authoring
- Constraint driven Device automatic placement
- Constraint driven net automatic routing
- Fast parasitic closure flow through rapid prototyping

## **Workshop – Program**

### **Wednesday, 26.09.2012**

12:30pm - 1:00pm Registration

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1:00pm – 3:00pm *Specification-driven Design with IC 6.1.5 ADE-XL*

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3:00pm – 3:30pm Break

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3:30p – 5:30pm *Specification-driven Design with IC 6.1.5 ADE-XL*

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### **Thursday, 27.09.2012**

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9:00am – 10:30am *Variability Aware Design and Analysis with IC 6.1.5 ADE-GXL*

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10:30am – 11:00am Break

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11:00am – 12:30pm *Variability Aware Design and Analysis with IC 6.1.5 ADE-GXL*

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12:30pm – 2:00pm Midday Break

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2:00pm – 3:30pm *Constraint-driven Analog Rapid Prototyping with IC 6.1.5*

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3:30pm – 4:00pm Break

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4:00pm – 5:30pm *Constraint-driven Analog Rapid Prototyping with IC 6.1.5*

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